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1 **The hardware results of a 64×64 analog input array for a 3-dimensi
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Duong, T.A.; Thomas, T.; Daud, T.; Thakoor, A.; Suddarth, S.;
Neural Networks Proceedings, 1998. IEEE World Congress on Computational
Intelligence. The 1998 IEEE International Joint Conference on , Volume: 1 , 4
May 1998
Pages:593 - 597 vol.1

[Abstract]   [PDF Full-Text (300 KB)]   **IEEE CNF**

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1 A SONET STS-3c user-network interface integrated circuit
Robe, T.J.; Walsh, K.A.;
Selected Areas in Communications, IEEE Journal on , Volume: 9 , Issue: 5 , Ju
1991
Pages:732 - 740

[Abstract]   [PDF Full-Text (764 KB)]   **IEEE JNL**

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1 **10 Gb/s framer/demultiplexer IC for SONET STS-192 applications**
Bagheri, M.; Kong, D.T.; Hacker, J.; Schneider, K.; Chow, J.;
Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995
May 1995
Pages:427 - 430

[Abstract]   [PDF Full-Text (520 KB)]   **IEEE CNF**

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2 **A SONET STS-3c user network interface integrated circuit**
Robe, T.J.; Walsh, K.A.;
Selected Areas in Communications, IEEE Journal on , Volume: 9 , Issue: 5 , Ju
1991
Pages:732 - 740

[Abstract]   [PDF Full-Text (764 KB)]   **IEEE JNL**